



51005.P200

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Collin et al.

Application No.: 09/731,535

Filed: December 6, 2000

For: CONVERSION OF AN HDL
SEQUENTIAL TRUTH TABLE
TO GENERIC HDL ELEMENTS

Examiner: Thompson, A.

Art Unit: 2825

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to Commissioner for Patents, Washington, DC 20231 on:

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Commissioner for Patents
Washington, D.C. 20231

AMENDMENT AND RESPONSE TO OFFICE ACTION

Sir:

In response to the Office Action mailed August 29, 2002, Applicant respectfully requests that the Examiner enter the following amendments and consider the following remarks.

IN THE ABSTRACT

Please replace the abstract with:

An HDL conversion process receives a data structure representing the behavior of a circuit element. The circuit element is sequential and the data structure is defined in a hardware description language (HDL) using a specific